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<u>L51</u>	L45 and l33	17	<u>L51</u>
<u>L50</u>	L45 and l32	9	<u>L50</u>
<u>L49</u>	L45 and l31	184	<u>L49</u>
<u>L48</u>	L45 and l30	22	<u>L48</u>
<u>L47</u>	L45 and l29	382	<u>L47</u>
<u>L46</u>	L45 and l28	560	<u>L46</u>
<u>L45</u>	L44 and (global\$5 or common or shar\$5) near4 address\$5	5345	<u>L45</u>
<u>L44</u>	(partition\$5 or cluster\$5) and (switch\$5 or mux or multiplex\$5 or crossbar\$1)	136641	<u>L44</u>
<u>L43</u>	L40 and l28	1372	<u>L43</u>
<u>L42</u>	L40 and address\$5	43	<u>L42</u>
<u>L41</u>	L40 and global near4 address\$5	0	<u>L41</u>

<u>L40</u>	(partition\$5 or cluster\$5) and (gateway\$1 or rout\$4) and (switch\$5 or mux or multiplex\$5 or crossbar\$1)	48194	<u>L40</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L39</u>	l27 and l33	3	<u>L39</u>
<u>L38</u>	l27 and l32	1	<u>L38</u>
<u>L37</u>	l27 and l31	15	<u>L37</u>
<u>L36</u>	l27 and l30	2	<u>L36</u>
<u>L35</u>	l27 and l29	42	<u>L35</u>
<u>L34</u>	l27 and l28	80	<u>L34</u>
<u>L33</u>	(712/29)[CCLS]	145	<u>L33</u>
<u>L32</u>	(370/355)[CCLS]	281	<u>L32</u>
<u>L31</u>	(710/107-317)![CCLS]	6976	<u>L31</u>
<u>L30</u>	(709/242)![CCLS]	466	<u>L30</u>
<u>L29</u>	(712/28-238)[CCLS]	9260	<u>L29</u>
<u>L28</u>	(712/2-300)[CCLS]	11961	<u>L28</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L27</u>	L26 and (gateway\$1 or rout\$4) and (switch\$5 or mux or multiplex\$5 or crossbar\$1)	806	<u>L27</u>
<u>L26</u>	(domain\$1 or cluster\$1) near5 hardware	1780	<u>L26</u>
<u>L25</u>	L24 and l4	25	<u>L25</u>
<u>L24</u>	(single or common or shar\$5) near5 exit\$3 and trac\$5 and entr\$4	2346	<u>L24</u>
<u>L23</u>	(traced or trace or tracing or traces) near25 entr\$4 near25 (common or single or one or merg\$6 or join\$5) near3 exit\$4	61	<u>L23</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L22</u>	L21 and l6	1	<u>L22</u>
<u>L21</u>	(714/11-41)[CCLS]	8292	<u>L21</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L20</u>	L19 not l13	28	<u>L20</u>
<u>L19</u>	L18 and l4	31	<u>L19</u>
<u>L18</u>	(trac\$5) near25 entr\$4 and (common or single or one or merg\$6 or join\$5) near3 exit\$4	1026	<u>L18</u>
<u>L17</u>	l16 and l4	31	<u>L17</u>
<u>L16</u>	(trac\$5) near25 entr\$4 and (common or single or one) near3 exit\$4	970	<u>L16</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L15</u>	(trac\$5) and entr\$4 and (common or single or one) near5 exit\$4	7826	<u>L15</u>
<u>L14</u>	6769126.pn. and exist\$3	1	<u>L14</u>
<u>L13</u>	L12 not l10	39	<u>L13</u>
<u>L12</u>	(trac\$5) near15 (table\$1 or stor\$5 or memor\$4 or ram\$1) near35 (common or single or one) near5 exit\$4	50	<u>L12</u>
<u>L11</u>	L10 not l9	1	<u>L11</u>
<u>L10</u>	(trac\$5) near15 (table\$1 or stor\$5 or memor\$4 or ram\$1) near35 single near4 exit\$4	11	<u>L10</u>

<u>L9</u>	L8 not l6	10	<u>L9</u>
<u>L8</u>	(trac\$5) near15 (table\$1 or stor\$5 or memor\$4 or ram\$1) near15 single near4 exit\$4	10	<u>L8</u>
<u>L7</u>	(trac\$5) near15 (table\$1 or stor\$5 or memor\$4 or ram\$1) near15 exit\$4	518	<u>L7</u>
<u>L6</u>	l5 and trac\$5	8	<u>L6</u>
<u>L5</u>	L4 and l1	8	<u>L5</u>
<u>L4</u>	(717/124-128)[CCLS]	1745	<u>L4</u>
<u>L3</u>	(717/124-128)![CCLS]	1745	<u>L3</u>
<u>L2</u>	(717/124-128)[CCLS]	1745	<u>L2</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L1</u>	(trac\$5) near8 (table\$1 or stor\$5 or memor\$4 or ram\$1) near8 exit\$4	415	<u>L1</u>

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 Bhattacharya, S.; Dey, S.; Brglez, F.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)
 Volume 15, Issue 9, Sept. 1996 Page(s):1088 - 1105
 Digital Object Identifier 10.1109/43.536715
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(1824 KB\)](#) IEEE JNL
[Rights and Permissions](#)
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 Gustavson, D.B.; Qiang Li;
[Communications Magazine, IEEE](#)
 Volume 34, Issue 8, Aug. 1996 Page(s):52 - 63
 Digital Object Identifier 10.1109/35.533919
[AbstractPlus](#) | [Full Text: PDF\(1788 KB\)](#) IEEE JNL
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 Arbaugh, W.A.; Davin, J.R.; Farber, D.J.; Smith, J.M.;
[Computer](#)
 Volume 31, Issue 9, Sept. 1998 Page(s):48 - 55
 Digital Object Identifier 10.1109/2.708450
[AbstractPlus](#) | [References](#) | [Full Text: PDF\(236 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **COFTA: hardware-software co-synthesis of heterogeneous distributed embedded systems f tolerance**
 Dave, B.P.; Jha, N.K.;
[Computers, IEEE Transactions on](#)
 Volume 48, Issue 4, April 1999 Page(s):417 - 441
 Digital Object Identifier 10.1109/12.762534

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(964 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

☐ **6. Quantitative characterization and analysis of the I/O behavior of a commercial distributed-st machine**

Bordawekar, R.R.;
[Parallel and Distributed Systems](#), [IEEE Transactions on](#)
Volume 11, Issue 5, May 2000 Page(s):509 - 526
Digital Object Identifier 10.1109/71.852403
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1015 KB\)](#) [IEEE JNL](#)
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☐ **7. Data monitoring in high-performance clusters for computing applications**

Torralba, G.; Gonzalez, V.; Sanchis, E.; Tao, J.; Schulz, A.; Karl, W.;
[Nuclear Science](#), [IEEE Transactions on](#)
Volume 49, Issue 2, Part 1, April 2002 Page(s):525 - 531
Digital Object Identifier 10.1109/TNS.2002.1003794
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(269 KB\)](#) [IEEE JNL](#)
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☐ **8. Architectures and performance of AWG-based optical switching nodes for IP networks**

Bregni, S.; Pattavina, A.; Vegetti, G.;
[Selected Areas in Communications](#), [IEEE Journal on](#)
Volume 21, Issue 7, Sept. 2003 Page(s):1113 - 1121
Digital Object Identifier 10.1109/JSAC.2003.815842
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1151 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

☐ **9. Analysis and optimisation of heterogeneous real-time embedded systems**

Pop, P.; Eles, P.; Peng, Z.;
[Computers and Digital Techniques](#), [IEE Proceedings-](#)
Volume 152, Issue 2, Mar 2005 Page(s):130 - 147
Digital Object Identifier 10.1049/ip-cdt:20045069
[AbstractPlus](#) | Full Text: [PDF\(730 KB\)](#) [IEEE JNL](#)

☐ **10. Performance of Hybrid Message-Passing and Shared-Memory Parallelism for Discrete Elem**

Henty, D.S.;
[Supercomputing](#), [ACM/IEEE 2000 Conference](#)
04-10 Nov. 2000 Page(s):10 - 10
Digital Object Identifier 10.1109/SC.2000.10005
[AbstractPlus](#) | Full Text: [PDF\(248 KB\)](#) [IEEE CNF](#)
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☐ **11. Sparsely hubbed light-trail grooming networks**

Balasubramanian, S.; Somani, A.K.; Kamal, A.E.;
[Computer Communications and Networks](#), [2005. ICCCN 2005. Proceedings. 14th International Co](#)
17-19 Oct. 2005 Page(s):249 - 254
Digital Object Identifier 10.1109/ICCCN.2005.1523860
[AbstractPlus](#) | Full Text: [PDF\(492 KB\)](#) [IEEE CNF](#)
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☐ **12. SMILE: An Integrated, Multi-Paradigm Software Infrastructure for SCI-Based Clusters**

Schulz, M.; Jie Tao; Trinitis, C.T.; Karl, W.;
[Cluster Computing and the Grid](#), [2002. 2nd IEEE/ACM International Symposium on](#)
21-24 May 2002 Page(s):247 - 247
Digital Object Identifier 10.1109/CCGRID.2002.1017133
[AbstractPlus](#) | Full Text: [PDF\(248 KB\)](#) [IEEE CNF](#)
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Himanshu Jain; Kroening, D.; Sharygina, N.; Clarke, E.;
[Design Automation Conference, 2005. Proceedings. 42nd](#)
13-17 June 2005 Page(s):445 - 450
[AbstractPlus](#) | Full Text: [PDF](#)(440 KB) [IEEE CNF](#)
[Rights and Permissions](#)
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Ulm, D.R.; Scherger, M.;
[Parallel and Distributed Processing Symposium, 2005. Proceedings. 19th IEEE International](#)
04-08 April 2005 Page(s):265a - 265a
Digital Object Identifier 10.1109/IPDPS.2005.412
[AbstractPlus](#) | Full Text: [PDF](#)(184 KB) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **15. Multithreaded home-based lazy release consistency over VIA**
Iosevich, V.; Schuster, A.;
[Parallel and Distributed Processing Symposium, 2004. Proceedings. 18th International](#)
26-30 April 2004 Page(s):59
Digital Object Identifier 10.1109/IPDPS.2004.1302984
[AbstractPlus](#) | Full Text: [PDF](#)(1376 KB) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **16. Compiling policy descriptions into reconfigurable firewall processors**
Lee, T.K.; Yusuf, S.; Luk, W.; Sloman, M.; Lupu, E.; Dulay, N.;
[Field-Programmable Custom Computing Machines, 2003. FCCM 2003. 11th Annual IEEE Symposi](#)
9-11 April 2003 Page(s):39 - 48
[AbstractPlus](#) | Full Text: [PDF](#)(508 KB) [IEEE CNF](#)
[Rights and Permissions](#)
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Mesa-Martinez, F.; Kaushik Narayanun; Hughey, R.;
[Computer Systems and Applications, 2003. Book of Abstracts. ACS/IEEE International Conference](#)
14-18 July 2003 Page(s):6
Digital Object Identifier 10.1109/AICCSA.2003.1227443
[AbstractPlus](#) | Full Text: [PDF](#)(159 KB) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **18. Information security standards for e-businesses**
Sattid, M.M.; Garner, B.J.; Nagrial, M.H.;
[Communication Systems, 2002. ICCS 2002. The 8th International Conference on](#)
Volume 2, 25-28 Nov. 2002 Page(s):641 - 645 vol.2
[AbstractPlus](#) | Full Text: [PDF](#)(392 KB) [IEEE CNF](#)
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Bruhl, L.; Rembold, B.;
[Vehicular Technology Conference, 2002. Proceedings. VTC 2002-Fall, 2002 IEEE 56th](#)
Volume 2, 24-28 Sept. 2002 Page(s):676 - 680 vol.2
Digital Object Identifier 10.1109/VETECF.2002.1040684
[AbstractPlus](#) | Full Text: [PDF](#)(242 KB) [IEEE CNF](#)
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Rakhmatov, D.N.; Vruthula, S.B.K.;
[Hardware/Software Codesign, 2002. CODES 2002. Proceedings of the Tenth International Sympos](#)
6-8 May 2002 Page(s):145 - 150
Digital Object Identifier 10.1109/CODES.2002.1003616

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Leung, S.; Postula, A.; Hemani, A.;
[Digital Systems Design, 2001. Proceedings. Euromicro Symposium on](#)
4-6 Sept. 2001 Page(s):326 - 333
Digital Object Identifier 10.1109/DSD.2001.952317
[AbstractPlus](#) | Full Text: [PDF](#)(728 KB) IEEE CNF
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Pragaspathy, H.; Falsafi, B.;
[Parallel Architectures and Compilation Techniques, 2000. Proceedings. International Conference on](#)
15-19 Oct. 2000 Page(s):47 - 56
Digital Object Identifier 10.1109/PACT.2000.888330
[AbstractPlus](#) | Full Text: [PDF](#)(968 KB) IEEE CNF
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Kang, J.; van der Werf, A.; Lippens, P.;
[System Synthesis, 2000. Proceedings. The 13th International Symposium on](#)
20-22 Sept. 2000 Page(s):207 - 213
Digital Object Identifier 10.1109/ISSS.2000.874051
[AbstractPlus](#) | Full Text: [PDF](#)(524 KB) IEEE CNF
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Dongming Jiang; Singh, J.P.;
[Computer Architecture, 1999. Proceedings of the 26th International Symposium on](#)
2-4 May 1999 Page(s):305 - 316
Digital Object Identifier 10.1109/ISCA.1999.765960
[AbstractPlus](#) | Full Text: [PDF](#)(3100 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **25. Performance driven architecture development: issues, methods, tools and paradigms**
Gerhardt, M.S.;
[Digital Avionics Systems Conference, 1999. Proceedings. 18th](#)
Volume 1/17 pp. vol.1, 24-29 Oct. 1999 Page(s):2.B.5-1 - 2.B.5-8 vol.1
Digital Object Identifier 10.1109/DASC.1999.863696
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Dwarkanadas, S.; Gharachorloo, K.; Kontothanassis, L.; Scales, D.J.; Scott, M.L.; Stets, R.;
[High-Performance Computer Architecture, 1999. Proceedings. Fifth International Symposium On](#)
9-13 Jan. 1999 Page(s):260 - 269
Digital Object Identifier 10.1109/HPCA.1999.744377
[AbstractPlus](#) | Full Text: [PDF](#)(172 KB) IEEE CNF
[Rights and Permissions](#)
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Ang, B.S.; Chiou, D.; Rudolph, L.; Arvind;
[High Performance Computing, 1998. HIPC '98. 5th International Conference On](#)
17-20 Dec. 1998 Page(s):228 - 237
Digital Object Identifier 10.1109/HIPC.1998.737993
[AbstractPlus](#) | Full Text: [PDF](#)(116 KB) IEEE CNF
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Mondragon-Torres, A.; Gonzalez-Carvajal, R.; Pineda de Gyvez, J.; Sanchez-Sinencio, E.;
[Cellular Neural Networks and Their Applications Proceedings, 1998. Fifth IEEE International Works](#)
14-17 April 1998 Page(s):398 - 403
Digital Object Identifier 10.1109/CNNA.1998.685411
[AbstractPlus](#) | Full Text: [PDF](#)(564 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **29. Parallel tree building on a range of shared address space multiprocessors: algorithms and performance**
Shan, H.; Singh, J.P.;
[Parallel Processing Symposium, 1998. 1998 IPPS/SPDP Proceedings of the First Merged Internat](#)
[Symposium on Parallel and Distributed Processing 1998](#)
30 March-3 April 1998 Page(s):475 - 484
Digital Object Identifier 10.1109/IPPS.1998.669959
[AbstractPlus](#) | Full Text: [PDF](#)(1064 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **30. Communication across fault-containment firewalls on the SGI origin**
Ghosh, K.; Christie, A.J.;
[High-Performance Computer Architecture, 1998. Proceedings. 1998 Fourth International Symposi](#)
1-4 Feb. 1998 Page(s):277 - 287

Digital Object Identifier 10.1109/HPCA.1998.650567

[AbstractPlus](#) | Full Text: [PDF](#)(180 KB) IEEE CNF

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31. An architecture based on the memory mapped node addressing in reconfigurable interconn

Ikeda, T.; Yamada, J.; Nonoyama, Y.; Kimura, J.; Yoshida, M.;

[Parallel Algorithms/Architecture Synthesis, 1997. Proceedings. Second Aizu International Symposium](#)
17-21 March 1997 Page(s):50 - 57

Digital Object Identifier 10.1109/AISPAS.1997.581625

[AbstractPlus](#) | Full Text: [PDF](#)(816 KB) IEEE CNF

[Rights and Permissions](#)



32. Reducing the communication overhead of dynamic applications on shared memory multiproc

Sivasubramaniam, A.;

[High-Performance Computer Architecture, 1997. Third International Symposium on](#)
1-5 Feb. 1997 Page(s):194 - 203

Digital Object Identifier 10.1109/HPCA.1997.569660

[AbstractPlus](#) | Full Text: [PDF](#)(1208 KB) IEEE CNF

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**33. COFTA: hardware-software co-synthesis of heterogeneous distributed embedded system ar
overhead fault tolerance**

Dave, B.P.; Jha, N.K.;

[Fault-Tolerant Computing, 1997. FTCS-27. Digest of Papers. Twenty-Seventh Annual International](#)
24-27 June 1997 Page(s):339 - 348

Digital Object Identifier 10.1109/FTCS.1997.614108

[AbstractPlus](#) | Full Text: [PDF](#)(1112 KB) IEEE CNF

[Rights and Permissions](#)



34. An open system architecture for integrated RF systems

Brousseau, R.; Sanders, A.; Huffman, D.R.; Abercrombie, H.; Hoffman, D.L.; Coleman, S.;

[Digital Avionics Systems Conference, 1997. 15th DASC. AIAA/IEEE](#)
Volume 1, 26-30 Oct. 1997 Page(s):5.1 - 1-5 vol.1

Digital Object Identifier 10.1109/DASC.1997.635082

[AbstractPlus](#) | Full Text: [PDF](#)(612 KB) IEEE CNF

[Rights and Permissions](#)



35. Synthetic Environment Tactical Integration Virtual Torpedo Project

Dotson, K.L.; Roberts, G.E.;

[OCEANS '97. MTS/IEEE Conference Proceedings](#)
Volume 2, 6-9 Oct. 1997 Page(s):1431 - 1436 vol.2

Digital Object Identifier 10.1109/OCEANS.1997.624207

[AbstractPlus](#) | Full Text: [PDF](#)(648 KB) IEEE CNF

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36. Affordable integrated sensor system

Rich, B.A.;

[Digital Avionics Systems Conference, 1996. 15th AIAA/IEEE](#)
27-31 Oct. 1996 Page(s):267 - 272

Digital Object Identifier 10.1109/DASC.1996.559168

[AbstractPlus](#) | Full Text: [PDF](#)(584 KB) IEEE CNF

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37. An object based data cache with conflict free concurrent access as shared memory for a pai

Kneip, J.; Pirsch, P.;

[VLSI Signal Processing IX, 1996. \[Workshop on\]](#)
30 Oct.-1 Nov. 1996 Page(s):25 - 34

Digital Object Identifier 10.1109/VLSISP.1996.558278

[AbstractPlus](#) | Full Text: [PDF](#)(648 KB) IEEE CNF
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**38. Circuit partitioning for distributed VHDL fault simulation**

Ryan, C.A.;

[ASIC Conference and Exhibit 1996. Proceedings. Ninth Annual IEEE International](#)
23-27 Sept. 1996 Page(s):255 - 258

Digital Object Identifier 10.1109/ASIC.1996.552005

[AbstractPlus](#) | Full Text: [PDF](#)(348 KB) IEEE CNF
[Rights and Permissions](#)

**39. Aspects of system modelling in Hardware/Software partitioning**

Voigt Knudsen, P.; Madsen, J.;

[Rapid System Prototyping 1996. Proceedings. Seventh IEEE International Workshop on](#)
19-21 June 1996 Page(s):18 - 23

Digital Object Identifier 10.1109/IWRSP.1996.506721

[AbstractPlus](#) | Full Text: [PDF](#)(520 KB) IEEE CNF
[Rights and Permissions](#)

**40. Real-time regularity detection for robot vision using a customized architectural approach**

Lee, C.Y.; Catthoor, F.; de Man, H.;

[Circuits and Systems 1990. IEEE International Symposium on](#)
1-3 May 1990 Page(s):1493 - 1496 vol.2

Digital Object Identifier 10.1109/ISCAS.1990.112416

[AbstractPlus](#) | Full Text: [PDF](#)(400 KB) IEEE CNF
[Rights and Permissions](#)

**41. Lynx/Galactica Net: a distributed, cache coherent multiprocessing system**

Wilson, A.; Teller, M.; Probert, T.; Dyung Le; LaRowe, R.;

[System Sciences 1992. Proceedings of the Twenty-Fifth Hawaii International Conference on](#)
Volume 1, 7-10 Jan. 1992 Page(s):416 - 426 vol.1

Digital Object Identifier 10.1109/HICSS.1992.183191

[AbstractPlus](#) | Full Text: [PDF](#)(932 KB) IEEE CNF
[Rights and Permissions](#)

**42. A heterogeneous multiprocessing computer system with shared memory**

Fong, A.S.; Wu, K.Y.; Chen, R.M.M.; Cheng, L.M.;

[TENCON '93. Proceedings. Computer, Communication, Control and Power Engineering 1993 IEEE](#)
[Conference on](#)

Issue 0, Part 10000, 19-21 Oct. 1993 Page(s):225 - 228 vol.1

Digital Object Identifier 10.1109/TENCON.1993.319969

[AbstractPlus](#) | Full Text: [PDF](#)(220 KB) IEEE CNF
[Rights and Permissions](#)

**43. A hardware platform for B-ISDN services multiplexing: design and performances of AAL and**

Marino, G.; Merli, E.; Pavesi, M.; Rigolio, G.; Vaccari, P.; Zizza, F.;

[Global Telecommunications Conference 1993. including a Communications Theory Mini-Conference](#)
[Conference Record, IEEE in Houston, GLOBECOM '93. IEEE](#)

29 Nov.-2 Dec. 1993 Page(s):1844 - 1848 vol.3

Digital Object Identifier 10.1109/GLOCOM.1993.318385

[AbstractPlus](#) | Full Text: [PDF](#)(460 KB) IEEE CNF
[Rights and Permissions](#)

**44. Structuring distributed shared memory with the π architecture**

Kulkarni, C. D.; Banerji, A.; Casey, M.R.; Cohn, D.L.;

[Distributed Computing Systems 1993. Proceedings the 13th International Conference on](#)
25-28 May 1993 Page(s):93 - 100

Digital Object Identifier 10.1109/ICDCS.1993.287719

[AbstractPlus](#) | Full Text: [PDF](#)(748 KB) IEEE CNF

[Rights and Permissions](#)**45. Parallel radiosity computation on a shared memory multiprocessor**

Singh, G.B.; Abraham, S.G.; Westervelt, F.H.;
Circuits and Systems, 1993. Proceedings of the 36th Midwest Symposium on
 16-18 Aug. 1993 Page(s):165 - 168 vol.1
 Digital Object Identifier 10.1109/MWSCAS.1993.343103

[AbstractPlus](#) | Full Text: [PDF](#)(420 KB) IEEE CNF

[Rights and Permissions](#)

**46. A simulation study of hardware-oriented DSM approaches**

Grujic, A.; Tomasevic, M.; Milutinovic, V.;
TENCON'94. IEEE Region 10's Ninth Annual International Conference. Theme: 'Frontiers of Comp
Proceedings of 1994
 22-26 Aug. 1994 Page(s):386 - 390 vol.1
 Digital Object Identifier 10.1109/TENCON.1994.369273

[AbstractPlus](#) | Full Text: [PDF](#)(436 KB) IEEE CNF

[Rights and Permissions](#)

**47. A software architecture for global address space communication on clusters: put/get on fas**

Giannini, L.A.; Chien, A.A.;
High Performance Distributed Computing, 1998. Proceedings. The Seventh International Symposi
 28-31 July 1998 Page(s):330 - 337
 Digital Object Identifier 10.1109/HPDC.1998.710019

[AbstractPlus](#) | Full Text: [PDF](#)(76 KB) IEEE CNF

[Rights and Permissions](#)

**48. MESS-a distributed operating system for the universe**

Dobosiewicz, W.; Eskicioglu, M.R.; Gburzynski, P.; Mutiso, A.;
Distributed Computing Systems, 1990. Proceedings. Second IEEE Workshop on Future Trends of
 30 Sept.-2 Oct. 1990 Page(s):208 - 214
 Digital Object Identifier 10.1109/FTDCS.1990.138322

[AbstractPlus](#) | Full Text: [PDF](#)(572 KB) IEEE CNF

[Rights and Permissions](#)

**49. A real-time operating environment for Army weapon systems**

Pritchett, W.W., IV; Bartkiewicz, J.D.;
Digital Avionics Systems Conference, 1999. Proceedings. 18th
 Volume 2, 24-29 Oct. 1999 Page(s):8.A.3-1 - 8.A.3-7 vol.2
 Digital Object Identifier 10.1109/DASC.1999.822003

[AbstractPlus](#) | Full Text: [PDF](#)(460 KB) IEEE CNF

[Rights and Permissions](#)

**50. An introduction to local area networks**

Clark, D.D.; Pograd, K.T.; Reed, D.P.;
Proceedings of the IEEE
 Volume 66, Issue 11, Nov. 1978 Page(s):1497 - 1517

[AbstractPlus](#) | Full Text: [PDF](#)(2599 KB) IEEE JNL

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- ☐ **51. A common transform engine for MPEG and AC3 audio decoder**
 Lau, W.; Chwu, A.;
[Consumer Electronics, IEEE Transactions on](#)
 Volume 43, Issue 3, Aug. 1997 Page(s):559 - 566
 Digital Object Identifier 10.1109/30.628677
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(652 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **52. Semantic search of learning services in a grid-based collaborative system**
 Vega-Gorgojo, G.; Bote-Lorenzo, M.L.; Gomez-Sanchez, E.; Dimitriadis, Y.A.; Asensio-Perez, J.I.;
[Cluster Computing and the Grid, 2005. CCGrid 2005. IEEE International Symposium on](#)
 Volume 1, 9-12 May 2005 Page(s):19 - 26 Vol. 1
 Digital Object Identifier 10.1109/CCGRID.2005.1558529
[AbstractPlus](#) | Full Text: [PDF](#)(2240 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **53. The use of product line based checkout systems for payload processing**
 Meade, P.T.; Myhand, C.;
[Digital Avionics Systems, 2001. DASC. The 20th Conference](#)
 Volume 2, 14-18 Oct. 2001 Page(s):8A4/1 - 8A4/11 vol.2
 Digital Object Identifier 10.1109/DASC.2001.964220
[AbstractPlus](#) | Full Text: [PDF](#)(833 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **54. The Quadrics network: high-performance clustering technology**
 Petrini, F.; Wu-chun Feng; Hoisie, A.; Coll, S.; Frachtenberg, E.;
[Micro, IEEE](#)
 Volume 22, Issue 1, Jan.-Feb. 2002 Page(s):46 - 57
 Digital Object Identifier 10.1109/40.988689
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(171 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **55. Embedded software in digital AM-FM chipset**
 Sarlotte, M.; Candaele, B.; Quevremont, J.; Merel, D.;
[Design Automation and Test in Europe Conference and Exhibition, 2003](#)
 2003 Page(s):340 - 341
 Digital Object Identifier 10.1109/DATE.2003.1253631

[AbstractPlus](#) | Full Text: [PDF\(226 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **56. A scheduling framework for system-level estimation**
Le Moullec, Y.; Diguet, J.P.; Philippe, J.L.;
[Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Conference on](#)
Volume 1, 17-20 Dec. 2000 Page(s):277 - 280 vol.1
Digital Object Identifier 10.1109/ICECS.2000.911536
[AbstractPlus](#) | Full Text: [PDF\(340 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **57. A high-level BIST synthesis method based on a region-wise heuristic for an integer linear pr**
Kim, H.B.; Ha, D.S.;
[Test Conference, 1999. Proceedings. International](#)
28-30 Sept. 1999 Page(s):903 - 912
Digital Object Identifier 10.1109/TEST.1999.805822
[AbstractPlus](#) | Full Text: [PDF\(860 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **58. System-level power optimization: techniques and tools**
Benini, L.; de Micheli, G.;
[Low Power Electronics and Design, 1999. Proceedings. 1999 International Symposium on](#)
1999 Page(s):288 - 293
[AbstractPlus](#) | Full Text: [PDF\(352 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **59. A new method for the minimization of memory area in high level synthesis**
Rouzeyre, B.; Sagnes, G.;
[Euro ASIC '91](#)
27-31 May 1991 Page(s):184 - 189
Digital Object Identifier 10.1109/EUASIC.1991.212869
[AbstractPlus](#) | Full Text: [PDF\(464 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **60. Signal processing and engineering analysis on personal computers**
South, H.M.; Dolecek, Q.E.;
[Proceedings of the IEEE](#)
Volume 73, Issue 12, Dec. 1985 Page(s):1826 - 1837
[AbstractPlus](#) | Full Text: [PDF\(1473 KB\)](#) IEEE JNL
[Rights and Permissions](#)
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Vasanthavada, N.; Marinos, P.N.;
[Computers, IEEE Transactions on](#)
Volume 37, Issue 4, April 1988 Page(s):440 - 448
Digital Object Identifier 10.1109/12.2188
[AbstractPlus](#) | Full Text: [PDF\(784 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **62. Analysis of convolutional encoders and synthesis of rate-2/n Viterbi decoders**
Summerfield, S.;
[Information Theory, IEEE Transactions on](#)
Volume 42, Issue 4, July 1996 Page(s):1280 - 1285
Digital Object Identifier 10.1109/18.508860
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(704 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ **63. Virtual reality-based training for the diagnosis of prostate cancer**

Burdea, G.; Patounakis, G.; Popescu, V.; Weiss, R.E.;

Biomedical Engineering, IEEE Transactions on

Volume 46, Issue 10, Oct. 1999 Page(s):1253 - 1260

Digital Object Identifier 10.1109/10.790503

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(196 KB) [IEEE JNL](#)

[Rights and Permissions](#)



64. Analysis of Hardware Acceleration in Reconfigurable Embedded Systems

Ouellette, M.; Connors, D.;

Parallel and Distributed Processing Symposium, 2005. Proceedings, 19th IEEE International

04-08 April 2005 Page(s):168a - 168a

Digital Object Identifier 10.1109/IPDPS.2005.107

[AbstractPlus](#) | Full Text: [PDF](#)(89 KB) [IEEE CNF](#)

[Rights and Permissions](#)



65. Application of UML for hardware design based on design process model

Damasevicius, R.; Stukys, V.;

Design Automation Conference, 2004. Proceedings of the ASP-DAC 2004, Asia and South Pacific

27-30 Jan. 2004 Page(s):244 - 249

[AbstractPlus](#) | Full Text: [PDF](#)(588 KB) [IEEE CNF](#)

[Rights and Permissions](#)



66. Simulation Bridge: a framework for multi-processor simulation

Nagendra, G.D.; Kumar, V.G.P.; Sheshadri, B.S.;

Hardware/Software Codesign, 2002. CODES 2002. Proceedings of the Tenth International Sympos

6-8 May 2002 Page(s):49 - 54

Digital Object Identifier 10.1109/CODES.2002.1003600

[AbstractPlus](#) | Full Text: [PDF](#)(481 KB) [IEEE CNF](#)

[Rights and Permissions](#)



67. Implementation of a fast algorithm to find data clusters

Qichun Xu; Martin, M.I.;

Nuclear Science Symposium Conference Record, 2001 IEEE

Volume 2, 4-10 Nov. 2001 Page(s):919 - 921 vol.2

[AbstractPlus](#) | Full Text: [PDF](#)(236 KB) [IEEE CNF](#)

[Rights and Permissions](#)



68. The Quadrics network (QsNet): high-performance clustering technology

Petrini, F.; Wu-chun Feng; Hoisie, A.; Coll, S.; Frachtenberg, E.;

Hot Interconnects 9, 2001.

22-24 Aug. 2001 Page(s):125 - 130

Digital Object Identifier 10.1109/HIS.2001.946704

[AbstractPlus](#) | Full Text: [PDF](#)(504 KB) [IEEE CNF](#)

[Rights and Permissions](#)



69. Portable code for complex critical systems

Audsley, N.C.; Bate, I.J.; Grigg, A.;

Real-Time Computing Systems and Applications, 1999. RTCSA '99. Sixth International Conference

13-15 Dec. 1999 Page(s):111 - 118

Digital Object Identifier 10.1109/RTCSA.1999.811200

[AbstractPlus](#) | Full Text: [PDF](#)(692 KB) [IEEE CNF](#)

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70. A path analysis based partitioning for time constrained embedded systems

Bianco, L.; Auguin, M.; Gogniat, G.; Pegatoquet, A.;

Hardware/Software Codesign, 1998. (CODES/CASHE '98) Proceedings of the Sixth International V

15-18 March 1998 Page(s):85 - 89

Digital Object Identifier 10.1109/HSC.1998.666242

[AbstractPlus](#) | Full Text: [PDF\(40 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)

☐ **71. An operation placement and scheduling scheme for cache and communication localities in architectures**

Yen-Kuang Chen; Kung, S.Y.;

[Parallel Architectures, Algorithms, and Networks, 1997 \(ISPAN '97\) Proceedings, Third International](#)

18-20 Dec. 1997 Page(s):390 - 396

Digital Object Identifier 10.1109/ISPAN.1997.645125

[AbstractPlus](#) | Full Text: [PDF\(544 KB\)](#) [IEEE CNF](#)

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☐ **72. Hyper-cluster: a scalable and reconfigurable wide-area lightwave network architecture**

Guo, D.; Acampora, A.S.; Zhang, Z.;

[Global Telecommunications Conference, 1997. GLOBECOM '97. IEEE](#)

Volume 2, 3-8 Nov. 1997 Page(s):863 - 867 vol.2

Digital Object Identifier 10.1109/GLOCOM.1997.638450

[AbstractPlus](#) | Full Text: [PDF\(612 KB\)](#) [IEEE CNF](#)

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☐ **73. A practical approach to the implementation of multiple radio configurations utilizing reconfigurable hardware and software building blocks**

Ivers, A.; Smith, D.;

[MILCOM 97 Proceedings](#)

Volume 3, 2-5 Nov. 1997 Page(s):1327 - 1332 vol.3

Digital Object Identifier 10.1109/MILCOM.1997.644983

[AbstractPlus](#) | Full Text: [PDF\(596 KB\)](#) [IEEE CNF](#)

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☐ **74. Hardware-software co-synthesis of fault-tolerant real-time distributed embedded systems**

Srinivasan, S.; Jha, N.K.;

[Design Automation Conference, 1995. with EURO-VHDL. Proceedings EURO-DAC '95. European](#)

18-22 Sept. 1995 Page(s):334 - 339

Digital Object Identifier 10.1109/EURDAC.1995.527426

[AbstractPlus](#) | Full Text: [PDF\(656 KB\)](#) [IEEE CNF](#)

[Rights and Permissions](#)

☐ **75. Comparative analysis of periodic state saving techniques in time warp simulators**

Fleischmann, J.; Wilsey, P.A.;

[Parallel and Distributed Simulation, 1995. \(PADS'95\). Proceedings. Ninth Workshop on \(Cat. No. 95](#)

14-16 June 1995 Page(s):50 - 58

Digital Object Identifier 10.1109/PADS.1995.404317

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Steenkiste, P.; Hemy, M.; Mummert, T.; Zill, B.;

[Computer Architecture. 1994. Proceedings the 21st Annual International Symposium on](#)

18-21 April 1994 Page(s):154 - 163

Digital Object Identifier 10.1109/ISCA.1994.288153

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